

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Hideyuki SHIMONISHI

Title: INTERPROCESSOR COMMUNICATION METHOD AND  
MULTIPROCESSOR SYSTEM

Appl. No.: Unassigned

Filing Date: 12/07/2000

Examiner: Unassigned

Art Unit: Unassigned

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56. A copy of each listed document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action, which would be appropriate to antedate or otherwise remove as a competent reference any document, which is determined to be a prima facie prior art reference against the claims of the present application.

**CONCISE EXPLANATION OF  
RELEVANCE OF EACH DOCUMENT**

Document A1 relates to Document A2.

The relevance of Documents A2 through A4 is described in the present specification. English translations of the Japanese documents are not readily available; however, English language abstracts are attached herewith. The absence of such translations does not relieve the PTO from its duty to consider the submitted documents (37 CFR § 1.98 and MPEP § 609).

The listed documents are being submitted in compliance with 37 C.F.R. § 1.97(b), within three (3) months of the filing date.

Applicant respectfully requests that the listed documents be considered by the Examiner and formally be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with MPEP § 609.

Respectfully submitted,

December 7, 2000

Date

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